IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Samir Chaudhry, et al.

Serial No.: 10/762,788

Filed: January 22, 2004

Title: MOS TRANSISTOR AND METHOD OF MANUFACTURE

Grp./A.U.: 2814

Examiner: Long Pham

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 I hereby certify that this correspondence is being electronically filed with United States Palent and undemark Office on.

April 4, 2006 (Date)

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| Signature of the person signing the certificate)

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REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.111

In response to the Examiner's Action mailed January 4, 2006, please reconsider the aboveidentified application for the following reasons: